

AP32158 - 070050.1280

41 2123 \$

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Kenneth Shepard  
Serial No. : 09/591,270  
Filed : June 9, 2000  
For : METHODS FOR ESTIMATING THE BODY VOLTAGE OF  
DIGITAL PARTIALLY DEPLETED SILICON-ON-  
INSULATOR CIRCUITS

Examiner : Ayal I. Sharon  
Art Unit : 2123

RESPONSE TO REQUIREMENT TO SUBMIT INFORMATION  
UNDER 37 C.F.R. § 1.105

I hereby certify that this paper is being deposited with the United States  
Postal Service as first class mail in an envelope addressed to:  
Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450

**RECEIVED**


JUL 07 2004

Technology Center 2100

June 24, 2004  
Date of Deposit

Paul Ackerman  
Attorney Name

39,891  
PTO Registration No

  
Signature

June 24, 2004  
Date of Signature

Commissioner for Patents  
P.O. Box 1450  
Arlington, VA 22313-1450

Dear Sir:

Applicants respectfully petition for a two-month extension of time to respond to  
the Office Action mailed on February 24, 2004. A check is enclosed to cover the  
extension fee.

The following remarks and Exhibits 1 and 2 are submitted in response to the

Examiner's requirement, pursuant to 37 C.F.R. § 1.105, that Applicants submit additional

06/30/2004 AAD0F01 00000097 09591270

01 FC:2252

210.00 DP

information concerning the declaration by the inventor, Dr. Kenneth Shepard, that was filed on December 8, 2003, pursuant to 37 C.F.R. § 1.131.